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FIG. 1

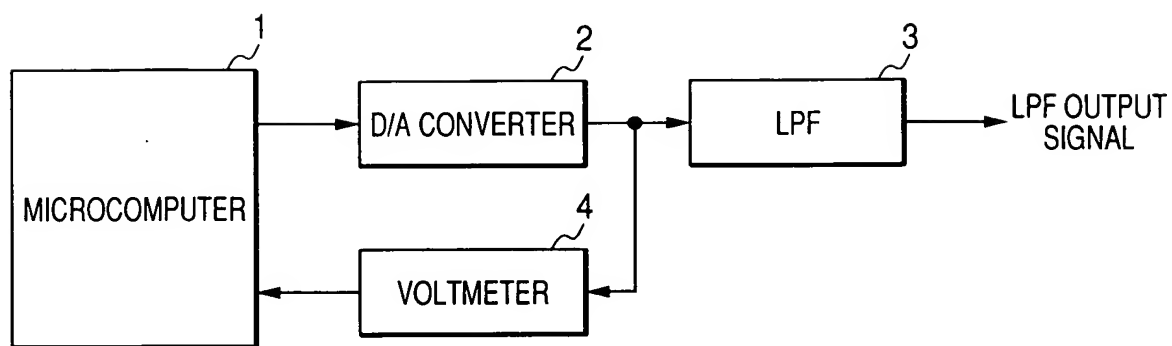
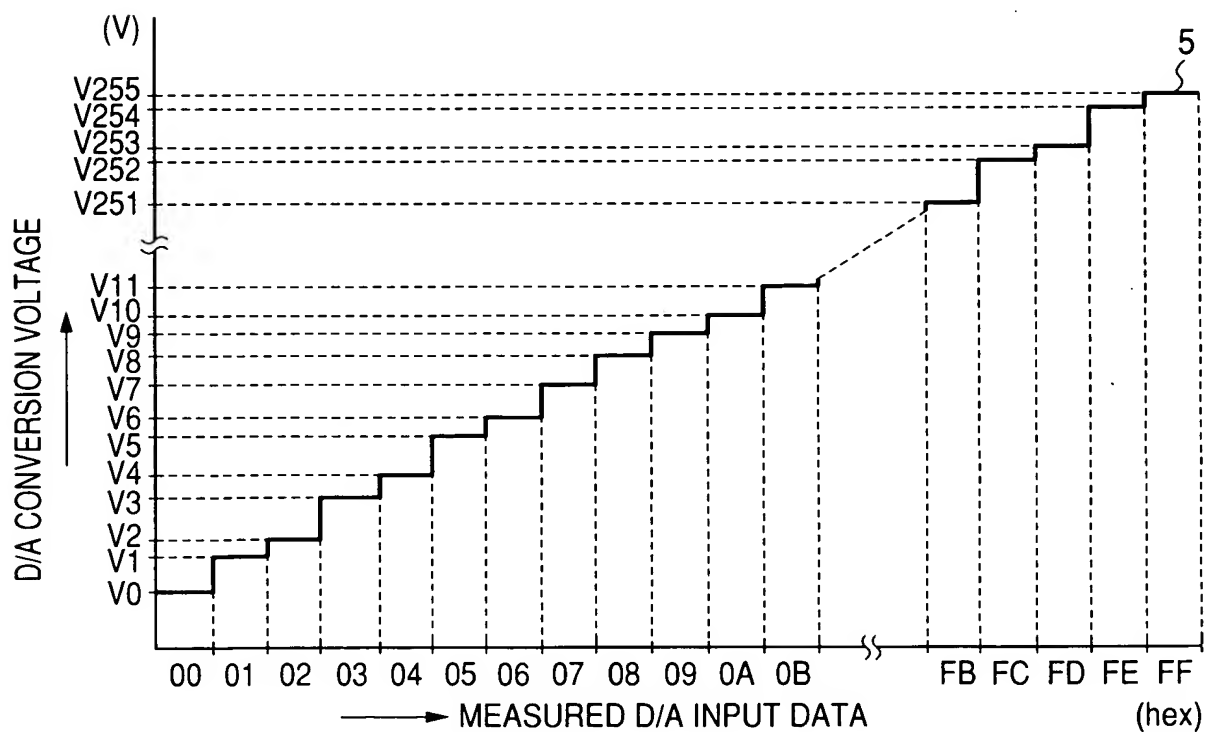
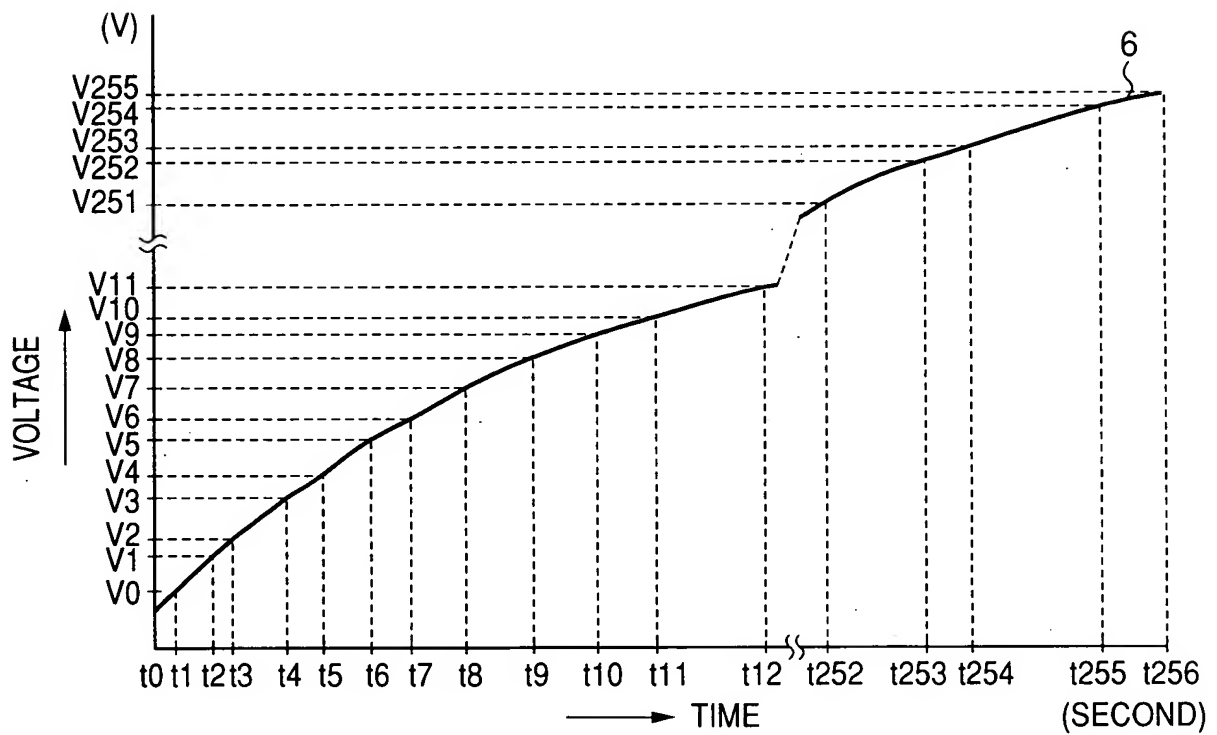


FIG. 2



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FIG. 3



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FIG. 4

STORAGE DATA IN THE MICROCOMPUTER	
TIMING	DESIRED D/A INPUT DATA
t1	00
t2	01
t3	02
t4	03
t5	04
t6	05
t7	06
t8	07
t9	08
t10	09
t11	0A
t12	0B
⋮	⋮
t252	FB
t253	FC
t254	FD
t255	FE
t256	FF

FIG. 5

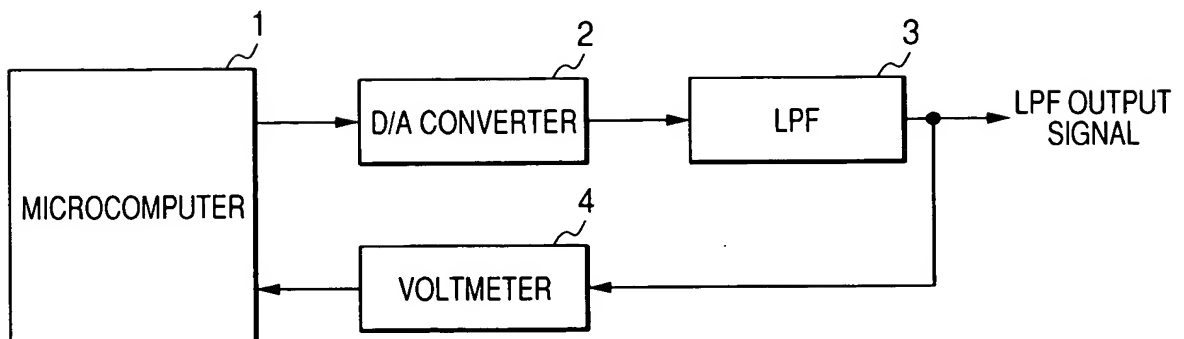


FIG. 6

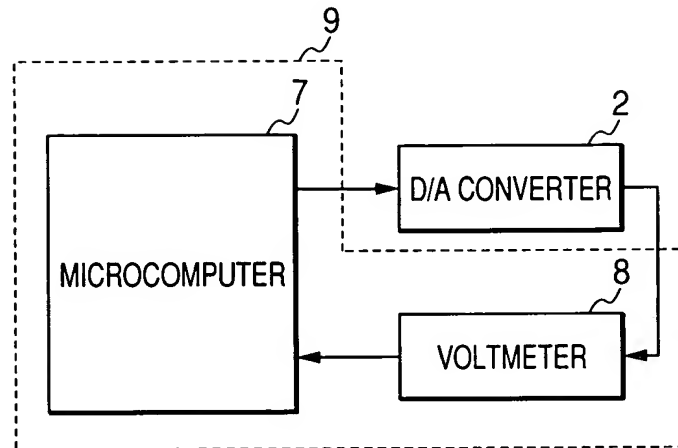
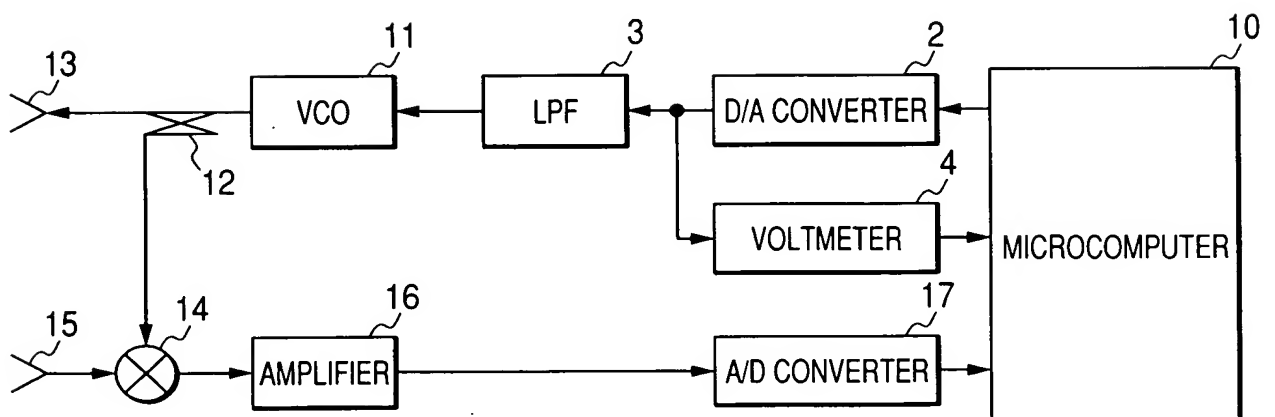


FIG. 7



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FIG. 8

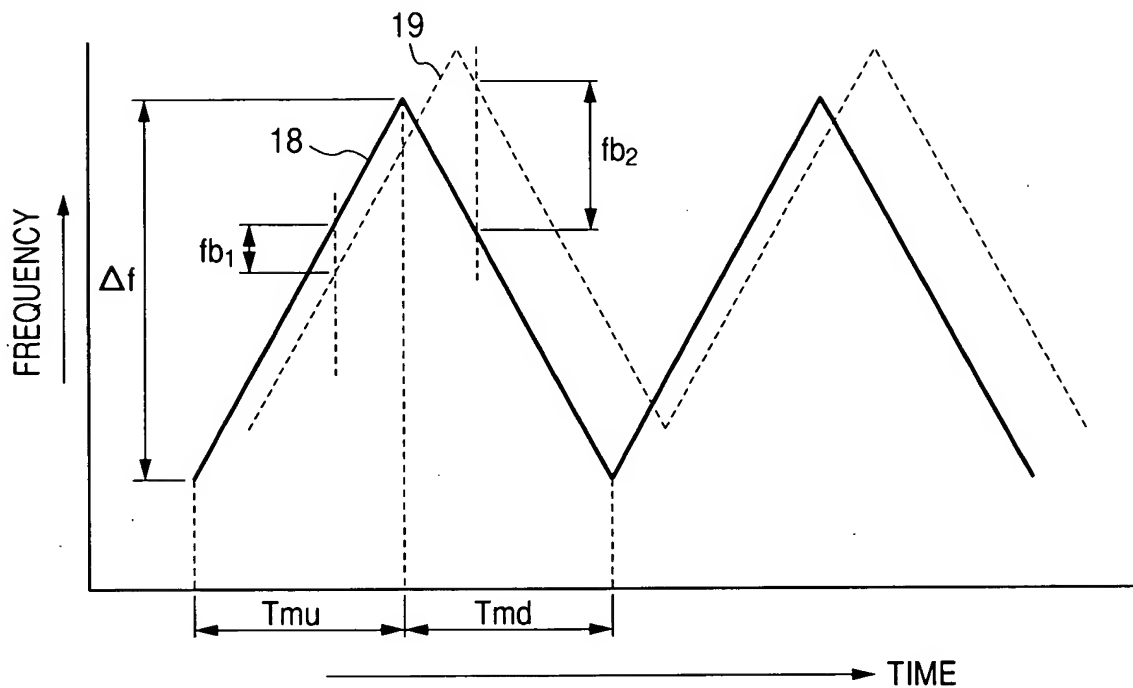


FIG. 9

